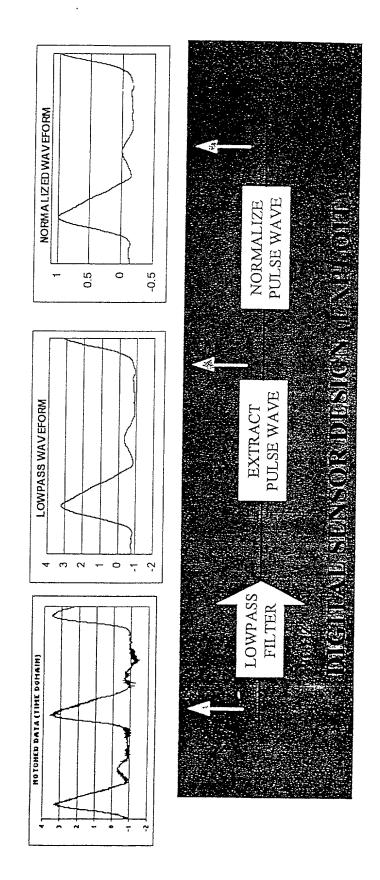


FIGURE 1

Digital Signal Processing

Flow Diagram



PIGURE 2

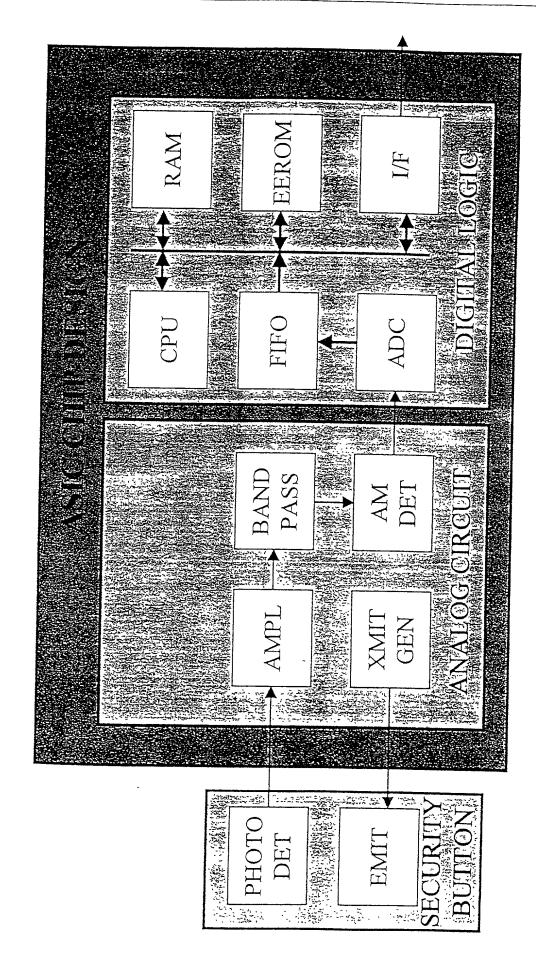


FIGURE 3